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	vork Reduction Act of 1995,	TIME UNDER 37 (JFR 1.136(a)	Docket Number ((Optional) CPAC 10
		In re A	pplication of Pen	idse	
			Application Number 10/081,491		Filed 02/22/2002
		For C	Chip Scale Packag	e with Flip Chip l	Interconnect
		Art Uni	t 2812	Examiner	Angel Roman
This is a request application.	under the provisions of	of 37 CFR 1.136(a)	to extend the perio	od for filing a reply	y in the above identific
The requested ex	tension and appropria	ate non-small-entity	fee are as follows	(check time perio	od desired):
One	e month (37 CFR 1.17	7(a)(1))			\$
™ Two	o months (37 CFR 1.1	17(a)(2))			<u>\$_420</u>
☐ Thr	ee months (37 CFR 1	.17(a)(3))			\$
☐ Fou	ır months (37 CFR 1.1	17(a)(4))			\$
☐ Five	e months (37 CFR 1.1	I7(a)(5))			\$
	e resulting fee is: \$				
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This collection of information is required by 37 CFR 1.136(a). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 6 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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